



ISSUE CLASSIFICATION	
Class	Subclass

City

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
ACCS! SCANNED	Q.A. CS!

TC 2
TC 856

APPLICATION NO. 09/657833	CONT/PRIOR	CLASS 375 381	SUBCLASS 111	ART UNIT 2631 2644	EXAMINER F. C. HARVEY
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Mr Timothy Shuttleworth

Add 10

System and method for synchronizing the clock frequencies of power processing devices and digital signal processing devices in an electronic system

PTO-2040
12/89

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	_____ (Assistant Examiner) _____ (Date)				
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____ _____ _____				ISSUE FEE	
	_____ _____ (Primary Examiner) _____ (Date)			Amount Due	Date Paid
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